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Image

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q83757

Tatsuo TSUJITA

Appln. No.: 10/508,795 ✓

Group Art Unit: 2819

Confirmation No.: 3903

Examiner: Brian K. Young

Filed: September 21, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT

**AMENDMENT UNDER 37 C.F.R. § 1.111**

**MAIL STOP AMENDMENT**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated February 22, 2005, please amend the above-identified application as follows on the accompanying pages.